

TRIDENT Approved Components List

Certificate-No. 968/EZ 101.06/08

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The following components have been approved by TÜV Rheinland Group and can be used in the TRIDENT v1.2, v1.3, v1.3.1, v1.4, v1.4.1, v1.5 system releases.

Trident Modules

Module Name	Model	FPGA Revision	Firmware Revision ²
Main Processor (MP) ¹	3101	04	Build 213
Communications Module (CM) ¹	3201	N/A	Build 213
Discrete Input, 24VDC, 32 points (DI)	3301	16, 0C	Build 213
Discrete Output, 24VDC, 16 points (DO)	3401	16, 08	Build 213
Analog Input, 24VDC, 0-5V, 32 points (AI)	3351	16, 02	Build 213
Analog Input/Digital Input, 24 VDC, 0-5V, 16 points (DI) + 16 points (AI)	3361	16, 02	Build 213
Relay Output (Solid State), 24VDC, 32 points (RO)	3451	16, 03	Build 213
Pulse Input, 6 points (PI)	3381	16, 21	Build 213
Analog Output, 4-20 mA, 4 points (RAO)	3481	16, 25	Build 213
Analog Output, 4-20 mA (2 points), 4-40 mA (2 points) (AO)	3482	16, 25	Build 213

Trident Baseplates

Module Name	Model
Main Processor (MP) Baseplate	2101
Communications Module (CM) Baseplate	2201
Discrete Input (DI) Baseplate	2301
Discrete Input (DI) Baseplate (with High Voltage Terminal Panel)	2302
Discrete Output (DO) Baseplate (Standard)	2401
Discrete Output (DO) Baseplate (Low Current)	2401L
Discrete Output (DO) Baseplate (with High Voltage External Termination Panel)	2402
Analog Input (AI) Baseplate	2351
Analog Input (AI) Baseplate (with External Termination Panel)	2352
Analog Input (AI) Baseplate with HART	2354
Analog Input (AI) Baseplate with HART - ATEX	2354A
Analog Input (AI) Baseplate (with External Termination Panel)	2352
Analog Input/Digital Input Combination (AI-DI) Baseplate	2631
Relate Output (RO) Baseplate	2451
Pulse Input (PI) Baseplate	2381



¹ These modules are approved according to IEC 1131-part2/1992, EN 61131-2/August 1994

² This revision reflect the latest firmware build of Trident v1.5 release.
 For an overview of compatible revisions for previous Trident versions refer to Trident Product Release Notice for Trident systems available on Triconex CustomerNet.

Module Name	Model
Analog Output (AO) Baseplate	2481
Analog Output (AO) Baseplate with HART	2483
Analog Output (AO) Baseplate with HART - ATEX	2483A
I/O Extender Module Kit (without termination)	2281
I/O Extender Module Kit for I/O Baseplate (with termination)	2291
I/O Extender Module Kit for MP Baseplate (with termination)	2292

Programming- and Software-Tools

Module Name	Revision
TriStation 1131 Developers Workbench, CEM Editor, Workbench Demo (Windows 2000, Windows XP)	4.3.505
Enhanced Diagnostic Monitor (EnDM)	2.1.153

	Manufacturer	Test Institute	Certifier
Signature			
	M. Kieu	O. Busa	H. Gall
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Document Revision

Date	Version	Revisions / Changes	Author
2008-10-17	1.1	Corrected wrong FPGA Revisions	Bu/ASI
2008-11-13	1.2	Added new Analog Input and Output baseplates 2354, 2354A, 2483 and 2483A according to Report-No.: 968/EZ101.08/08	Bu/ASI